

DEC 08 2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. .... 10/809,420  
Filing Date ..... March 26, 2004  
Inventor ..... Zhongze Wang  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2812  
Examiner ..... Jennifer M. Kennedy  
Attorney Docket No. .... MI22-2534  
Title: Silicon-On-Insulator Comprising Integrated Circuitry and Methods of  
Forming Silicon-On-Insulator Circuitry

Commissioner for Patents

**ATTENTION:** Examiner Jennifer M. Kennedy  
Group Art Unit 2812

Alexandria, VA 22313-1450

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Dated: December 8, 2004

By:

Muriel G. Dunnigan

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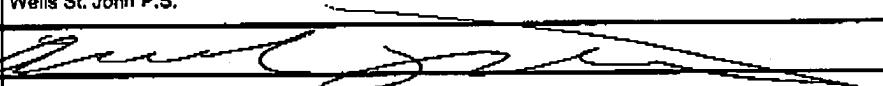
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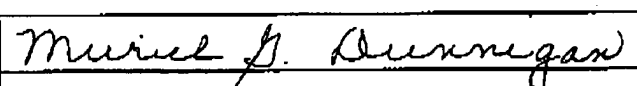
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<b>TRANSMITTAL FORM</b>	Application Number	10/808,420	<b>RECEIVED CENTRAL FAX CENTER DEC 08 2004</b>
	Filing Date	March 28, 2004	
	First Named Inventor	Zhongze Wang	
	Art Unit	2812	
	Examiner Name	Jennifer M. Kennedy	
(to be used for all correspondence after initial filing)		Attorney Docket Number	MI22-2534
Total Number of Pages in This Submission		3	

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Firm Name	Wells St. John P.S.		
Signature			
Printed name	Mark S. Matkin		
Date	12-8-04	Reg. No.	32,268

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Typed or printed name	Muriel G. Dunnigan	Date December 8, 2004

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**STATEMENT OF THE SUBSTANCE OF THE INTERVIEW**

To: Commissioner for Patents  
**ATTENTION:** Examiner Jennifer M. Kennedy  
Group Art Unit 2812  
Alexandria, VA 22313-1450

**VIA FACSIMILE**

From: Mark Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
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Spokane, WA 99201-3828

Responsive to the telephone interview dated November 10, 2004,

Applicant remarks as follows:

**REMARKS**

An interview was conducted between the undersigned and Examiner Jennifer M. Kennedy on November 10, 2004. A Patent Office requirement for a terminal disclaimer was discussed, and such was provided by facsimile to the Examiner.

Respectfully submitted,

Dated: 12-8-04

By: 

Mark S. Matkin  
Reg. No. 32,268